



Docket No. 24061.142 (TSMC2002-1387)  
Customer No. 42717

2811  
Bfw

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ai-Sen Liu, et al.

Serial No.: 10/657,847

Filed: September 9, 2003

For: Method for Forming Dielectric  
Barrier Layer in Damascene  
Structure

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Docket Number: 24061.142  
(TSMC2002-1387)

Examiner: To Be Determined

Art Unit: 2811

Conf. No.: 2138

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SUPPLEMENTAL SHEET TO POWER OF ATTORNEY

In accordance with 37 CFR 1.32 (c)(3) and with reference to the accompanying Power of Attorney, please recognize the following ten patent practitioners as being of record in this application.

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

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7-23-04  
Date



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Liu, et al.

Serial No. Unknown

Filed: Herewith

For: METHOD FOR FORMING DIELECTRIC  
BARRIER LAYER IN DAMASCENE  
STRUCTURE

§ Attorney Docket No.: 24061.142  
§  
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§ Customer No. 27683  
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§ Group Art Unit: Unknown  
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§ Examiner: Unknown  
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Commissioner for Patents  
Washington, D.C. 20231

POWER OF ATTORNEY FOR PATENT APPLICATION

As a representative of the Assignee, Taiwan Semiconductor Manufacturing Company, Ltd., I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

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The undersigned is the representative for the Assignee of the entire right, title, and interest in the patent application identified above. A copy of the assignment or other documents in the chain of title are attached.

The undersigned (whose title is supplied below) is authorized to act on behalf of the Assignee.

Taiwan Semiconductor Manufacturing, Inc.

9/26/03  
Date

  
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Title: Director, IP Division

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